Syllabus for CSCI 351 Intro Computer Systems Fall 2014

Cary G. Gray Office: Science 159, x5875 Home: $omitted \ online$ E-mail: $omitted \ online$ Office hours: MWF 9:15–10:15 a.m. M 3:00–5:00 p.m. F 2:00–3:00 p.m. TW afternoons by appointment

You are welcome to stop in when my office door is open.

Class meetings

MWF 8:00–9:05 a.m., Science 131 Final exam: 8:00–10:00 a.m. on Tuesday, Dec. 16

On-line resources

Additional (and updated) course information will be available at the class page at

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http://cs.wheaton.edu/~cgray/csci351/
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Texts

Bryant and O'Hallaron, Computer Systems: A Programmer's Perspective/2e, Prentice Hall, 2010.

I also recommend that you have access to some reference for the need to have a C (not C++) language and library. The following is a very good reference:

Harbison and Steele, C: A Reference Manual, fifth edition, Prentice-Hall, 2002.

Description

CSCI 351 Introduction to Computer Systems

An introduction to low-level systems issues from the perspective of the programmer. Representation of both data and program as produced by a compiler; hardware support for memory, input/output, and parallelism; fundamental ideas in operating systems and networking.

Prerequisite: CSCI 245.

By the end of this class, you should gain:

- 1. Facility in programming in C, including use of the standard libraries;
- 2. An understanding of the different levels at which programs and data are represented;
- 3. Ability to program in light of low-level understanding, especially with regard to representation and performance;
- 4. An understanding of key abstractions provided by programming languages and operating systems, including at least one way in which they can be implemented; and
- 5. Ability to use standard vocabulary to describe system features.

Exams and written homework will largely cover items 2, 4, and 5; programming assignments will include items 1 and 3 as well.

Grading. Your grade will be based on the following:

- 50% exams
- 10% participation (including attendance)
- 40% homework (including programs)

There will be two in-class exams (planned for Oct 13 and Nov 17) in addition to the final. The final exam will be weighted more heavily than the in-class exams.

Note that your course grade may be lowered up to one letter for disruptive behavior or recurrent absence or tardiness.

Policies

Attendance. I expect you to be in class, and you are responsible for what happens in class whether you are present or not. If you are sick or must miss because of other school responsibilities, let me know in advance. If there is an emergency, let me know as soon as practical. You must make arrangements for make-up work (including exams) *in advance*; after-the-fact arrangements will be permitted only for emergencies, and then only with your timely notification and, in the case of illness, confirmation by the Health Center. (You'll do well to think of this as practice for keeping a job. Note that you can reach me by e-mail, and my office phone takes messages at all hours.)

I also expect you to be on time. If you come in late, don't interrupt class, and plan to find out what you've missed from another student *after* class. If the door is locked when you arrive, do not disturb the class.

Your presence in class calls for your full attention. That implies no use of networked devices to be virtually somewhere else. *If* you need to use a computer of some sort for access to your textbook or to take notes, you should do so with the networking turned off, and you are implicitly promising that you will not use it for any other purpose during class.

Preparation. You are expected to read assigned material, as indicated in the class schedule, before each class meeting: what we do in class will assume that you've done the reading. As you read, note anything that you aren't sure about, and bring your questions to class.

Projects Programming assignments are scheduled so that you should be able to complete them a day or two before the due date. Plan to complete them early; that way you'll have time to ask questions about them in class, and you'll have time to deal with surprises.

Written homework will be due at the beginning of class, though we may take time to discuss it before it is collected. As with the programs, the due dates are generally set to allow you to attempt the homework a class meeting before it is due. All homework should be neat, clearly labeled, and written on one side only of letter-sized paper; multiple pages should be stapled together.

Be sure you turn in what you have on time: I do not promise to give credit for late assignments.

Academic integrity. I expect you to conduct yourself honestly in this course. When you submit work, you assert that it is your own. If you use an outside source or receive assistance, acknowledge it. Deliberate misrepresentation will result in no credit for the assignment; a second offense will result in failing the course. All offenses will be reported and are subject to college disciplinary action as well.

Because you are encouraged to work together and provide each other assistance, you do risk inadvertent plagiarism. Be cautious, especially when you ask for or provide assistance. Make sure that you don't let someone else do your work for you, and make sure that you don't do someone else's work. Should you discover that a program in one of the textbooks is similar enough to an assignment to provide a useful starting point, that is fine–if you understand it fully and cite it. In all cases, simply be sure that you acknowledge clearly whatever help you receive.

There are two things that you can do that will eliminate the bulk of the temptations in this area. The first is to avoid procrastination: assignments are given with plenty of time for you to ask questions in class or in my office. The second is to be careful how you ask for help: do not ask other students to tell you the

solutions to assigned problems; ask questions to figure out what it is you do not yet understand and in order to understand it. (When giving help, don't be lazy and give away the answers; help your fellow students to learn.)

You are also responsible for ensuring that your work for this course isn't available for others to copy without your permission. You will need to make sure that file and directory permissions are set properly in the lab.

Special circumstances and needs Wheaton College is committed to providing reasonable accommodations for students with disabilities. Any student with a documented disability needing academic adjustments is requested to contact the Academic and Disability Services Office as early in the semester as possible. Call 630.752.5941 or email jennifer.nicodem@wheaton.edu for further information.

Gender-neutral language For academic discourse, spoken and written, the faculty expects students to use gender inclusive language for human beings.

Initial schedule

Here is our initial schedule; watch in class and online for updates.

Date	Reading	
Aug 27		Review and introduction
29	ch. 1, 2.1	Memory I: Representation
		bits, etc.; integers
Sep 1	Labor 1	Day
3	Moore, 2.2–3	signs, arithmetic
		start L1 (due Sep 17)
5	2.4-	floating-point
8		
10	3 1-4	Processor and Programs I
	0.1 4	basic architecture, operands
12	3.5, 3.8-9	primitive operations, structures in memory
15	3.6	control structures
10		start L2 (due Oct 1)
17	3.7	functions
19	3.10-	jumps, conditions
22	3.10 -	loops, switch, proc. call
24		wrapping up instructions
26		Memory II: Allocation
		procedure call
	9.11	address space; stacks and heaps
		start L3 (due Oct 17)
Oct 1		System Model I: Protection and Abstraction
	0.1	basics of input/output
3	8.1	interrupts and exceptions, handlers
6	8.2	process, kernel; system call
8	8.3-5, 8.7	process management
10		Review
13		Exam
15	6.1, 10.1 - 3	System Model II: I/O
		files, file descriptors
17	10.4-	reusing the fu abstraction
		start L4 (due Nov 12)

Date	Reading	
20	Quad br	eak
22	91-262-3	Memory III: Caches and Translation
	5.1 2, 0.2 5	Address spaces, caches
24	6.4	Cache organization
27	6.5 -	Cache consequences
29		pragmatics
31	9.3–9.6	Address translation
		start L5 (due Nov 19)
Nov 3	0.0_10	Memory IV: Allocators
	5.5 10	data structures
5		
7		
10	19 3_4	System Model III: Concurrency
10	12.0 4	Threads
12		coordination
1.4		start L6 (due Dec 10)
14		Review
17		Exam
19	11 1-4	System V: Networks
		addressing, sockets
21	11.5-	RPCs
24		Processors II: Parallelism
		Multiprocessors
26-28	Thanksg	iving
Dec 1		memory consistency
3		Instruction-level parallelism
5	start ch. 5	code improvement (optimization)
8		performance
10		What lies ahead?
12		Review
8:00-10:00 a.m., Tues 16 Dec		Final exam