

Schedule (recent and immanent)

Date	Reading	In class
Wed, Nov 13	4.2. Computing with TMs 4.3 Extensions to TMs 4.4 RAM TMs	Through definition of semidecide
Fri, Nov 15	4.5 Nondet. TMs	Through 4.5
Mon, Nov 18	5.1 C-T Thesis 5.2 Universal TMs 5.3 The halting problem 5.4 Undecidable problems	Through 5.3
Wed, Nov 20	5.6 Tiling 5.6 Properties of rec langs	5.(4,6,7)
Fri, Nov 22	6 (whole chapter)	Loose ends of Ch 5 Begin Ch 6

Definition 4.2.4: Let $M = (K, \Sigma, \delta, s, H)$ be a Turing machine, $\Sigma_0 \subseteq \Sigma - \{\sqcup, \triangleright\}$ be an alphabet and $L \subseteq \Sigma_0^*$ be a language.

▶ M **semidecides** L if

$$\forall w \in \Sigma_0^*, w \in L \text{ iff } M \text{ halts on } w$$

▶ L is **recursively enumerable** iff there exists a Turing machine that semidecides L .

§4.3. Extensions to Turing machines

- ▶ The extensions to Turing machines don't change the model's power
- ▶ These extensions make certain results easier

Why add multiple tapes to a Turing machine?

- ▶ It's interesting that it adds no power
- ▶ Sometimes it's easier to use
 - ▶ Theorem 4.4.2, reducing a RAM to a $(k + 3)$ -tape machine
 - ▶ Theorem 4.5.1, reducing a nondeterministic Turing machine to a 3-tape machine



§4.4. Random access Turing machines (RATMs or RAMs)

Definition 4.4.1: A RATM is a pair $M = (k, \Pi)$, where k is the number of registers and Π is a list of instructions.

A **configuration** is a $k + 2$ tuple, $(\kappa, R_0, R_1, \dots, R_{k-1}, T)$. Note that κ , the program counter, is the Greek kappa.

Theorem 4.4.1: Any recursive or recursively enumerable language, and any recursive function, can be decided, semidecided, and computed, respectively, by a random access Turing machine.

Theorem 4.4.2: Any language decided or semidecided by a random access Turing machine, and any function computable by a random access Turing machine, can be decided, semidecided, and computed, respectively, by a standard Turing machine in polynomial steps.

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Proof sketch.

- ▶ *One tape for input*
- ▶ *One tape for the store*
- ▶ *k tapes for registers*
- ▶ *One tape as “scratch space”*
- ▶ *One tape to rule them all, one tape to find them, one tape to bring them all and in the darkness bind them.*

In the land of Mordor, where the Turing machine lies.

§4.5. Nondeterministic Turing machines

Three ways to think of nondeterminism: Oracular knowledge, Searching with back-tracking, and bifurcation.

Decision and semidecision for nondeterministic Turing machines

Decide	For all computations	the machine <i>must</i> halt (For w there exists a finite bound N on the length of any computation)
	For all $w \in L$	there exists a computation that halts y (Some may halt n)
	For all $w \notin L$	<i>no</i> computations halt y (<i>All</i> computations halt n)
Semidecide	Some computations may not halt	
	For all $w \in L$	there exists a computation that halts
	For all $w \notin L$	<i>no</i> computations halt

Prob 4.5.1.a

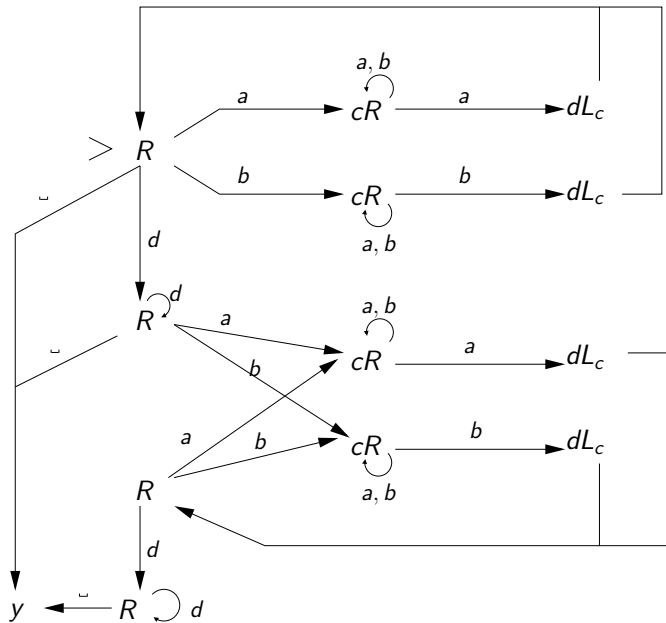
Consider the regular expression to be broken up in the following phases:

$$\underbrace{a^*}_1 \underbrace{a}_2 \underbrace{b}_3 \underbrace{b^*}_4 \underbrace{b}_5 \underbrace{a}_6 \underbrace{a^*}_7$$

Consider those phases states. Then we can make the Turing machine as

$(1, a, 1, \rightarrow)$	$(1, a, 2, \rightarrow)$
$(2, a, 3, \rightarrow)$	$(3, b, 4, \rightarrow)$
$(4, b, 4, \rightarrow)$	$(4, b, 5, \rightarrow)$
$(5, b, 6, \rightarrow)$	$(6, a, 7, \rightarrow)$
$(7, a, 7, \rightarrow)$	$(7, \sqcup, h,)$

Prob 4.5.1.b



Proof of Theorem 4.5.1, continued.

Define M' with three tapes: One for the original input, one for the tape of the current simulation of M , and one for the current hint tape for M_d . Algorithm for M' :

copy input onto the simulation tape

put 1 onto the hint tape

L: Operate like M_d

if you ever halt, then great!

if you run out of hints,

copy original input back to the simulation tape

put the *lexicographically next* hint on the hint tape

goto L